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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Vugranam C. Sreehar
Docket No.: YOR920010262US2
Serial No.: 09/925,580
Filing Date: August 9, 2001
Group: 219
Examiner: Ted T. Vo

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *[Signature]* Date: December 21, 2005

Title: Method and Apparatus for Programming Software Components

TRANSMITTAL OF APPEAL BRIEF

Mail Stop Appeal Brief - Patents
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Appeal Brief; and
2. Copy of Notice of Appeal, filed on October 21, 2005, with copy of stamped return postcard indicating receipt of Notice by PTO on October 24, 2005.

There is an additional fee of \$500 due in conjunction with this submission under 37 CFR §1.17(c). Please charge **IBM Corporation's Deposit Account No. 50-0510** the amount of \$500 to cover this fee. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation's Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully,

Kevin M. Mason

Date: December 21, 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Vugranam C. Sreedhar
Docket No.: YOR920010262US2
Serial No.: 09/925,580
Filing Date: August 9, 2001
Group: 2192
Examiner: Ted T. Vo

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Signature: [Signature] Date: December 21, 2005

Title: Method and Apparatus for Programming Software Components

APPEAL BRIEF

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals the final rejection dated May 23, 2005, of claims 2-4, 10-12, and 18 of the above-identified patent application.

REAL PARTY IN INTEREST

The present application is assigned to International Business Machines Corporation, as evidenced by an assignment recorded on August 9, 2001 in the United States Patent and Trademark Office at Reel 012067, Frame 0529. The assignee, International Business Machines Corporation, is the real party in interest.

RELATED APPEALS AND INTERFERENCES

There are no related appeals or interferences.

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STATUS OF CLAIMS

The present application was filed on August 9, 2001 with claims 1 through 20. Claims 1, 5-7, 9, 13-15, 17, and 19 were cancelled in the Amendment and Response to Office Action dated August 26, 2004. Claims 8, 16, and 20 were cancelled in the
5 Amendment and Response to Office Action dated September 15, 2005. Claims 2-4, 10-12, and 18 are presently pending in the above-identified patent application. Claims 2-4 remain rejected under 35 U.S.C. §101 because the claimed invention is directed to non-statutory subject matter. Claims 2-4, 10-12, and 18 remain rejected under 35 U.S.C. §102(b) as being anticipated by Magee et al. ("Composing Distributed Object in
10 CORBA," 1997 IEEE (CiteSeer)).

STATUS OF AMENDMENTS

The amendments in the Amendment and Response to Office Action dated September 15, 2005 have been entered.

SUMMARY OF CLAIMED SUBJECT MATTER

The present invention is directed to a method and apparatus for programming software components that treats software components as the basic unit of abstraction and computation (page 4). A software component is encapsulated and classes
20 and other program entities, such as data fields and methods, within a given component do not exist beyond a component boundary (pages 4-6 and 8-13). A component interacts with other components only by means of a defined set of input and output ports (page 4). A component can inherit and implement ports declared in a template and can declare and implement new ports (page 5). A component can only access the external environment
25 through its output ports. An output port of one component can only be connected to a conforming input port of another component. A connect statement is an explicit plumbing operation for connecting components together (pages 14-15). Interactions between components are loosely coupled (page 9). A related set of templates can be grouped together to form a group. Groups are useful for implementing implicit
30 invocation and multicasting (page 11).

STATEMENT OF GROUNDS OF REJECTION TO BE REVIEWED ON APPEAL

Claims 2-4 are rejected under 35 U.S.C. §101 because the claimed invention is directed to non-statutory subject matter. Claims 2-4, 10-12, and 18 are rejected under 35 U.S.C. §102(b) as being anticipated by Magee et al.

5

ARGUMENTSection 101 Rejections

Claims 2-4 were rejected under 35 U.S.C. §101 because the claimed invention is directed to non-statutory subject matter and because the cited claims are
 10 manipulating an abstract idea. Regarding claim 4, the Examiner asserts that the claim is not tangibly embodied.

The Supreme Court has stated that the "[t]ransformation and reduction of an article 'to a different state or thing' is the clue to patentability of a process claim." *Gottshalk v. Benson*, 409 U.S. 63, 70, 175 U.S.P.Q. (BNA) 676 (1972). In other words,
 15 claims that require some kind of transformation of subject matter, which has been held to include intangible subject matter, such as data or signals, that are representative of or constitute physical activity or objects have been held to comply with Section 101. *See, for example, In re Warmerdam*, 31 U.S.P.Q.2d (BNA) 1754, 1759 n.5 (Fed. Cir. 1994) or *In re Schrader*, 22 F.3d 290, 295, 30 U.S.P.Q.2d (BNA) 1455, 1459 n.12 (Fed. Cir.
 20 1994).

Thus, as expressly set forth in each of the independent claims, the claimed methods or system describe a method for programming a software component that instantiates a software component and attaches an input port to a class utilizing an attach command, thereby transforming the instantiated software component. This instantiation
 25 of a software component and transformation of the software component (and input port) provides a useful, concrete and tangible result.

In addition, claim 4 recites that the method is *executed by a processor*. Claim 12 is directed to a system and claim 18 is directed to an article of manufacture. Applicant believes that each of claims 2-4 are in full compliance with 35 U.S.C. §101
 30 and, accordingly, Applicant respectfully requests that the rejection under 35 U.S.C. §101 be withdrawn.

Independent Claims 4, 12 and 18

Independent claims 4, 12, and 18 were rejected under 35 U.S.C. §102(b) as being anticipated by Magee et al. Regarding claim 4, the Examiner asserts that Magee discloses utilizing an attach command to (Magee discloses tools to develop structure systems of components shown in FIGS. 1-4, where the structure shown in these figures includes input ports of class), attach at least one of said at least one input port to a class (FIGS. 1-4 show input ports are attached in components). In the Advisory Action, the Examiner asserts that the specification's description "based on the short mentioning of the above passage does not patentable different to the components and the components' attachment shown in the reference."

Appellant notes that Magee does not utilize an explicit command to attach an input port to a class, and thus the implementation *cannot change at run time*. The present invention *utilizes an explicit attach command*. Contrary to the Examiner's assertion, the utilization of an explicit attach command is patentable subject-matter and is well supported in the specification. For example, the present disclosure teaches that "a component *must attach each of its input ports* to a concrete class within it." (Page 3, lines 3-4; emphasis added.) The present disclosure also teaches that "each input port 112 in a component 110 should be attached (*using the attach command*) to some concrete class with in the component 110." (Page 12, lines 5-6; emphasis added.) Finally, the present disclosure teaches that

the following code segment defines a component 110, referred to as BooleanComp, implementing the template, BooleanTempl, defined above:

```

component BooleanComp implements BooleanTempl {
  attach xin BoolClass ; // attach input port xin to class
  BoolClass ;
  BooleanComp() {...} // constructors.
  class BoolClass implements Bool {
    boolean not(boolean x) { ...} ;
    boolean nand (boolean x, boolean y) {...}
  }
}

```

Thus, the input port xin is "attached" to the class BoolClass.

(Page 6, line 23, to page 7, line 6; emphasis added.)

Independent claims 4, 12, and 18 require utilizing an attach command to attach at least one of said at least one input port to a class.

Thus, Magee does not disclose or suggest utilizing an attach command to attach at least one of said at least one input port to a class, as required by independent
5 claims 4, 12, and 18.

Conclusion

The rejections of the cited claims under section 102 in view of Magee et al. are therefore believed to be improper and should be withdrawn. The remaining
10 rejected dependent claims are believed allowable for at least the reasons identified above with respect to the independent claims.


The attention of the Examiner and the Appeal Board to this matter is appreciated.

Respectfully,

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Date: December 21, 2005

20


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APPENDIX

1. (Cancelled)

5 2. The method of claim 4, further comprising the step of
allowing said software component to access an external environment only through said
output port.

 3. The method of claim 4, further comprising the step of
10 allowing a client to access said software component only through said input port.

 4. A method executed by a processor for programming a
software component, said method comprising the steps of:
 defining properties of said software component, including at least one
15 input port and at least one output port;
 providing a software mechanism for instantiating said software
component; and
 utilizing an attach command to attach at least one of said at least one input
port to a class.

20

5. (Cancelled)

6. (Cancelled)

25 7. (Cancelled)

8. (Cancelled)

9. (Cancelled)

30

10. The system of claim 12, wherein said processor is further configured to allow said software component to access an external environment only through said output port.

5 11. The system of claim 12, wherein said processor is further configured to allow a client to access said software component only through said input port.

10 12. A system for programming a software component, said system comprising:
a memory that stores computer-readable code; and
a processor operatively coupled to said memory, said processor configured to implement said computer-readable code, said computer-readable code configured to:
define properties of said software component, including at least one input
15 port and at least one output port;
provide a software mechanism for instantiating said software component;
and
utilize an attach command to attach at least one of said at least one input
port to a class.

20

13. (Cancelled)

14. (Cancelled)

25

15. (Cancelled)

16. (Cancelled)

17. (Cancelled)

30

18. An article of manufacture for programming a software component, said system comprising:

a computer readable medium having computer readable code means embodied thereon, said computer readable program code means comprising:

5 a step to define properties of said software component, including at least one input port and at least one output port;

a step to provide a software mechanism for instantiating said software component; and

10 a step to utilize an attach command to attach at least one of said at least one input port to a class.

19. (Cancelled)

20. (Cancelled)

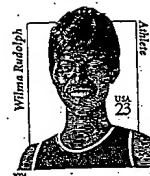
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EVIDENCE APPENDIX

There is no evidence submitted pursuant to § 1.130, 1.131, or 1.132 or entered by the Examiner and relied upon by appellant.

RELATED PROCEEDINGS APPENDIX

There are no known decisions rendered by a court or the Board in any proceeding identified pursuant to paragraph (c)(1)(ii) of 37 CFR 41.37.

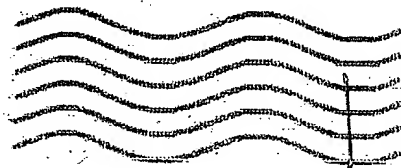


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Receipt in the USPTO is hereby acknowledged of:

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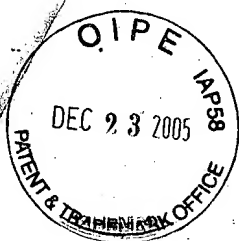
Transmittal Letter -- (Original & 1 copy)
Notice of Appeal - (Original & 1 copy)
Petition for Extension of Time -- (Original & 1 copy)

October 21, 2005
Serial No.: 09/925,580
YOR920010262US2
1500-223 (KMM)

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NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES		Docket Number (Optional) YOR920010262US2	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Assistant Commissioner for Patents, Washington D.C. 20231" on <u>October 24, 2005</u> . Signature <u>Susan Fortuna</u> Typed or printed name <u>Susan Fortuna</u>		In re Application of <u>Sreedhar et al.</u>	
		Application Number <u>09/925,580</u>	Filed <u>August 9, 2001</u>
		For <u>Method and Apparatus for Programming Software Components</u>	
		Group Art Unit <u>2191</u>	Examiner <u>Ted T. Vo</u>

Applicant hereby **appeals** to the Board of Patent Appeals and Interferences from the last decision of the examiner.

The fee for this Notice of Appeal is (37 CFR 1.17(b)) \$ 500.00.

- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is: \$ _____.
- ☐ A check in the amount of the fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☐ The Commissioner has already been authorized to charge fees in this application to a Deposit Account. I have enclosed a duplicate copy of this sheet.
- ☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 50-0510. I have enclosed a duplicate copy of this sheet.
- ☒ A petition for an extension of time under 37 CFR 1.136(a) (PTO/SB/22) is enclosed.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

I am the

- ☐ applicant/inventor.
- ☐ assignee of record of the entire interest.
See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)
- ☒ attorney or agent of record.
- ☐ attorney or agent acting under 37 CFR 1.34(a).
Registration number if acting under 37 CFR 1.34(a) _____.

Kevin M. Mason
Signature

Kevin M. Mason
Typed or printed name

October 24, 2005
Date

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☐ *Total of 1 forms are submitted.

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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